



**UNIVERSITY OF
PLYMOUTH**

PROGRAMME QUALITY HANDBOOK 2025-26

FdSc Electrical Electronic Engineering

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1. Welcome and Introduction to FdSc Electrical Electronic Engineering.

Welcome to FdSc Electrical Electronic Engineering delivered at Oceansgate campus by City College Plymouth.

This programme has been designed to develop and practically apply a broad knowledge base of electrical and electronic systems theory as well as essential skills required in the field of Electrical and Electronic Engineering.

Students will initially study a range of underpinning theories covering science, mathematics, electrical and electronic principles, management theory and design and microprocessor control. Candidates will then progress on to more advanced applications of the theories in areas including Electrical Power, Analogue and Digital Electronics and Industrial Control and Mechatronics. Students will also undertake a Work-Based Project to practically demonstrate the essential management and research skills required at this level of study and within the workplace. This will be driven by close liaison with employers to ensure that delivery is both current and relevant, thus enhancing the employability skills of students.

This programme has been designed to equip you with the skills and knowledge base required to work in your chosen specialism or other graduate opportunities. It is also a platform from which you can undertake additional vocational and academic qualifications.

This Programme Quality handbook contains important information including:
The approved programme specification
Module records

Note: The information in this handbook should be read in conjunction with the current edition of:

- Your Programme Institution & University Student Handbook which contains student support based information on issues such as finance and studying at HE
 - available in your Google Classroom
 - o Your Module, Teaching, Learning and Assessment Guide
 - available in your Google Classroom
- University of Plymouth's Student Handbook
 - o available at:
<https://www.plymouth.ac.uk/your-university/governance/student-handbook>

2. Programme Specification

1. FdSc Electrical and Electronic Engineering

Final award title FdSc Electrical and Electronic Engineering

Level X Intermediate award title(s) N/A

Level X Intermediate award title(s) N/A

UCAS code H602

JACS code H600

2. Awarding Institution: University of Plymouth

Teaching institution(s): City College Plymouth

3. Accrediting body(ies)

Summary of specific conditions/regulations

Date of re-accreditation

4. Distinctive Features of the Programme and the Student Experience

The Foundation Degree programme comprises 12 core modules of 240 credits which is equivalent to the first two years of study on a Degree level programme

This programme has been designed to develop and practically apply a broad knowledge base of electrical and electronic systems theory as well as essential skills required in the field of Electrical and Electronic Engineering.

Students will initially study a range of underpinning theories covering science, mathematics, electrical and electronic principles, management theory and design and microprocessor control. Candidates will then progress on to more advanced applications of the theories in areas including Electrical Power, Analogue and Digital Electronics and Industrial Control and Mechatronics. Students will also undertake a Work-Based Project to practically demonstrate the essential management and research skills required at this level of study and within the workplace. This will be driven by close liaison with employers to ensure that delivery is both current and relevant, thus enhancing the employability skills of students.

Delivery will be supported by practical activities using industry standard hardware and software development environments within specialist workshop/ laboratory areas and

supplemented by practical activities to allow for evaluation of modern design tools and methods. This will take full advantage of the College's £13m investment in the state-of-the-art Regional Centre of Excellence for STEM.

A range of assessment methods are used to ensure that students have gained a thorough grounding in not only the underlying principles but also how they apply in practical, industrial applications. Close links have been established with local industries which drives the development and continuous updating of the course. This ensures that the skills learnt are relevant to employment in the engineering sector both locally and globally. It also provides the underpinning research based academic skills required of managers within industry and to allow successful candidates to continue into further, higher level studies. It is a college commitment to maintain on-going links with appropriate industries to ensure that assessment and delivery are, where appropriate, practical and work-based.

Delivery is planned to be flexible to accommodate both our part time and full time students. Employed, part time students will undertake a day release delivery model to ensure that the impact on employers is kept to a minimum.

5. Relevant QAA Subject Benchmark Group(s)

The subject benchmark statement (2015)¹ defines the academic standard expected of graduates with an engineering degree. The defined learning outcomes are those published by the Engineering Council in the UK-SPEC UK standard for professional engineering competence www.engc.co.uk Third edition²,

The QAA, foundation degree benchmarks³ and the QAA Quality Code⁴

1. <http://www.qaa.ac.uk/en/Publications/Documents/SBS-engineering-15.pdf>
2. [http://www.engc.org.uk/engcdocuments/internet/Website/UK-SPEC%20third%20edition%20\(1\).pdf](http://www.engc.org.uk/engcdocuments/internet/Website/UK-SPEC%20third%20edition%20(1).pdf)
3. <http://www.qaa.ac.uk/en/Publications/Documents/Foundation-Degree-qualification-benchmark-May-2010.pdf>
4. <http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

6. Programme Structure

The Programme of study comprises of 240 module credits across level 4 and level 5 with 120 credits per level. The aim of the programme is to develop skills consistent with Engineering Council and Engineering Subject Benchmarks. Due to our strong links with employers in the city and high number of part time learners who are already employed in industry our programme has been developed to provide for the varied roles across the city as Electrical Electronic Engineers, as well as provide a solid grounding to our full time students wishing to further their study or enter employment.

**8. Programme Structure for the Foundation Degree in Electrical and Electronic Engineering (full-time) New Programme Structure
2025/26**

Stage 1			
Module Code	Module Title	No. of Credits	Core / Optional
CITY 1077	Engineering Mathematics	20	Core
CITY 1078	Engineering Science	20	Core
CITY 1079	Digital and Analogue Devices and Circuits	20	Core
CITY 1080	Project Design and Business Management	20	Core
CITY 1081	Electrical and Electronic Principles	20	Core
CITY 1082	Microprocessor Systems and High Level Programming	20	Core
Stage 2			
CITY 2075	Electrical Power	20	Core
CITY 2076	Further Analogue Electronics	20	Core
CITY 2077	Further Digital Electronics	20	Core
CITY 2078	Applications of Power Electronics	20	Core
CITY 2079	Work-based Learning Project	20	Core
CITY 2081	Industrial Control Systems and Mechatronics	20	Core

9. Programme Structure for the Foundation Degree Electrical Electronic Engineering (part-time)

Stage 1			
Module Code	Module Title	No. of Credits	Core / Optional
CITY 1077	Engineering Mathematics	20	Core
CITY 1078	Engineering Science	20	Core
CITY 1079	Digital and Analogue Devices and Circuits	20	Core
CITY 1080	Project Design and Business Management	20	Core
Stage 2			
CITY 1081	Electrical and Electronic Principles	20	Core
CITY 1082	Microprocessor Systems and High Level Programming	20	Core
CITY 2075	Electrical Power	20	Core
CITY 2076	Further Analogue Electronics	20	Core
Stage 3			
CITY 2077	Further Digital Electronics	20	Core
CITY 2078	Applications of Power Electronics	20	Core
CITY 2079	Work-based Learning Project	20	Core
CITY 2081	Industrial Control Systems and Mechatronics	20	Core

7. Programme Aims

This programme in line with the Engineers Council aims to:

1. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems.
2. Develop knowledge and skills to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.
3. Develop knowledge of the business implications of engineering decisions and a knowledge of the inter-relationship between the market, engineering activities and the management structures
4. Prepare the students to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.

8. Programme Intended Learning Outcomes

9.1. Knowledge and understanding

On successful completion graduates should have developed:

- 1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice.
- 2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain, decommission and re-cycle electrical / electronic engineering processes, systems, services and products.
- 3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.

8.2. Cognitive and intellectual skills

On successful completion graduates should have developed the ability to:

- 1) Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.
- 2) Analyse results to solve electrical and electronic engineering problems, apply technology and implement solutions.
- 3) Implement design solutions and contribute to their evaluation.

8.3. Key and transferable skills

On successful completion graduates should have developed the ability to:

- 1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.
- 2) Work independently or as a member of a team.

8.4. Employment related skills

On successful completion graduates should have developed:

- 1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to achieve flexibility within the work environment.
- 2) Knowledge of relevant codes of conduct.
- 3) Appropriate Work based learning through Projects and practical assessments.

8.5. Practical skills

On successful completion graduates should have developed:

- 1) The ability to select and use appropriate equipment to perform engineering tests.
- 2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits.

10. Admissions Criteria, including APCL, APEL and DAS arrangements

All applicants must have GCSE (or equivalent) Maths and English at Grade C or above.

Entry Requirements for FdSc Electrical Electronic Engineering	
A-level/AS-level	Normal minimum entry requirements are 56 on new UCAS Tariff at A-level to include Grade D in Maths or Physics
BTEC National Diploma/QCF Extended Diploma	Candidates are interviewed before an offer is made. But an equivalent of 56 UCAS points in an Engineering Subject
Access to Higher Education at level 3	Candidates are interviewed before an offer is made. Pass an Access to HE Diploma in Science with an equivalent of 56 UCAS points
Welsh Baccalaureate	Normal minimum entry requirements are an equivalent of 56 on new UCAS Tariff include Maths, Physics or Engineering

Scottish Qualifications Authority	Normal minimum entry requirements are an equivalent of 56 on new UCAS Tariff include Maths, Physics or Engineering
Irish Leaving Certificate	Normal minimum entry requirements are an equivalent of 56 on new UCAS Tariff include Maths, Physics or Engineering
International Baccalaureate	Normal minimum entry requirements are an equivalent of 56 on new UCAS Tariff include Maths, Physics or Engineering
Non Standard Qualifications with experience	All non-standard applicants are interviewed by the tutor and screened centrally to ensure impartial oversight.

Level 5 entry:

Progression from the HNC Electrical and Electronic Engineering is guaranteed, If a student is studying part time, and know they wish to progress they will study two level 5 modules in addition to the level 4 modules in order to complete the Part time FdSc course within 3 years, this has been approved by exception to regulations. The two level 5 modules which will be studied as a short course are:

CITY 2075	Electrical Power	20 credits
CITY 2076	Further Analogue Electronics	20 credits

11. Progression

Until the college has acquired partial accreditation through IET Students will not be able to progress on to the 3rd year of the BSc in Electrical and Electronic Engineering due to the requirements of IET accreditation with the University. All progressing students will only be allowed progression on to year 2 at the University until accreditation has been approved.

12. Exceptions to Regulations

Through application the college has been granted exception to regulations to allow part time students to APCL 120 level 4 credits and a further 40 credits at level 5 into year 3 of FdSc Electrical and Electronic Engineering if they have passed the HNC and studied a further 40 credits of level 5 modules as a short course during their part time year 2 of study. The two level 5 modules will be :
CITY2075 Electrical Power and CITY 2076 Further Analogue Electronics.

13. Transitional Arrangements

The College is currently delivering both an HNC and FdSc Electrical and Electronic Engineering. It is planned that all students currently enrolled on these programmes will

remain enrolled on the old programme structure to support in ensuring the meeting of programme level learning outcomes. Any student moving from old HNC will transfer to old FdSc.

However – due to the identified issues with over-assessment at element level the team will be submitting minor changes for some key modules on the old programme structure to support in ensuring alignment with the Plymouth University Assessment Policy for existing students.

All new students from September 2017 will enrol on the completely new structure.

14. Mapping and Appendices:

14.1. ILO's against Modules Mapping (Template attached)

Please see appendix 13.1

14.2. Assessment against Modules Mapping

Please see appendix 13.2

14.3. Skills against Modules Mapping

Please see appendix 13.3

13.4 Work Based Learning Mapping

please see appendix 13.4

Appendix 13.1 – Learning Outcomes map

LEVEL 4				
FHEQ Descriptors	Subject Benchmark(s)	Programme Aims	Programme Outcomes	Core Modules linked to outcomes
<p>Students will have demonstrated: Knowledge of the underlying concepts and principles associated with their areas of study;</p> <p>Ability to evaluate and interpret these within the context of that area of study;</p>	<p>A2, Use appropriate scientific, technical or engineering principles.</p>	<p>1. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems.</p>	<p>1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice.</p>	<p>CITY1077 Engineering Mathematics CITY1078 Engineering Science CITY1079 Digital and Analogue Devices and Circuits CITY1080 Project Design and Business Management CITY1081 Electrical and Electronic Principles CITY1082 Microprocessors and High Level Programming</p>
	<p>A1, Review and select appropriate techniques, procedures and methods to undertake tasks. B1, Identify problems and apply appropriate methods to identify causes and achieve satisfactory solutions. B2, Identify, organise and use resources effectively to complete tasks, with consideration for cost, quality, safety, security and environmental impact. D1, Use oral, written and electronic methods for</p>	<p>1. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills. 2. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems. 4. Provide the opportunity to 'learn through design' via practical and</p>	<p>8.1.2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain, decommission and re-cycle electrical / electronic engineering processes, systems, services and products. 8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.</p>	<p>CITY1077 Engineering Mathematics CITY1078 Engineering Science CITY1079 Digital and Analogue Devices and Circuits CITY1080 Project Design and Business Management</p>

<p>Ability to present, evaluate and interpret qualitative and quantitative data;</p>	<p>the communication in English1 of technical and other information.</p> <hr/> <p>D2, Present and discuss proposals.</p>	<p>project based work, particularly within the context of electrical and electronic design.</p> <hr/> <p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<hr/> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.</p>	<hr/> <p>CITY1080 Project Design and Business Management</p>
<p>Students will be able to: Evaluate the appropriateness of different approaches to solving problems related to their area of study;</p>	<p>A1, Review and select appropriate techniques, procedures and methods to undertake tasks. A2, Use appropriate scientific, technical or engineering principles. B1, Identify problems and apply appropriate methods to identify causes and achieve satisfactory solutions.</p> <hr/> <p>D1, Use oral, written and electronic methods for the communication in English1 of technical and other information.</p>	<p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p> <hr/> <p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement. 8.2.1) The ability to Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.</p> <hr/> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.</p>	<p>CITY1077 Engineering Mathematics CITY1078 Engineering Science CITY1080 Project Design and Business Management CITY1082 Microprocessors and High Level Programming</p> <hr/> <p>CITY1080 Project Design and Business Management CITY1082 Microprocessors and High Level Programming</p>
<p>Communicate the results of their study accurately and reliably and with structured and coherent argument</p>	<p>E4, Carry out and record CPD necessary to maintain and enhance competence in own area of practice including:</p>	<p>1. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the</p>	<p>8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to</p>	<p>CITY1077 Engineering Mathematics CITY1078 Engineering Science CITY1079 Digital and Analogue Devices and Circuits CITY1080 Project Design and Business Management</p>

	<ul style="list-style-type: none"> • Undertake reviews of own development needs • Plan how to meet personal and organisational objectives • Carry out planned (and unplanned) CPD activities • Maintain evidence of competence development • Evaluate CPD outcomes against any plans made • Assist others with their own CPD. 	<p>analysis and design of electronic, electric power and renewable energy systems.</p> <p>2. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.</p> <p>3. Provide an awareness of the business implications of engineering decisions and a knowledge of the inter-relationship between the market, engineering activities and the management structures</p> <p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<p>achieve flexibility within the work environment.</p>	<p>CITY1081 Electrical and Electronic Principles CITY1082 Microprocessors and High Level Programming</p>
<p>Students will also have: The qualities and transferable skills necessary for employment requiring the exercise of some personal responsibility</p>	<p>C1, Work reliably and effectively without close supervision, to the appropriate codes of practice. E1, Comply with the Code of Conduct of your institution. E2, Manage and apply safe systems of work.</p>	<p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<p>8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to achieve flexibility within the work environment.</p>	<p>CITY1077 Engineering Mathematics CITY1078 Engineering Science CITY1079 Digital and Analogue Devices and Circuits CITY1080 Project Design and Business Management CITY1081 Electrical and Electronic Principles CITY1082 Microprocessors and High Level Programming</p>

LEVEL 5				
FHEQ Descriptors	Subject Benchmark(s)	Programme Aims	Programme Outcomes	Core Modules linked to outcomes
<p>Students will have demonstrated: Knowledge and critical understanding of the well-established principles of their area of study and the way in which those principles have developed;</p>	<p>A1, Maintain and extend a sound theoretical approach to the application of technology in engineering practice. B2, Contribute to the design and development of engineering solutions. B3, Implement design solutions and contribute to their evaluation.</p>	<p>1. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems. 2. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.</p>	<p>8.1.1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice. 8.1.2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain, decommission and re-cycle electrical / electronic engineering processes, systems, services and products. 8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.</p>	<p>CITY2075 Electrical Power CITY2076 Further Analogue Electronics CITY2077 Further Digital Electronics CITY2078 Applications of Power Electronics CITY2079 Work-based Learning Project CITY2081 Industrial Control Systems and Mechatronics</p>
<p>Ability to apply underlying concepts and principles outside the context in which they were first studied, including where appropriate, the application of those principles in an employment context;</p>	<p>A2, Use a sound evidence-based approach to problem-solving and contribute to continuous improvement. B1, Identify, review and select techniques, procedures and methods to undertake engineering tasks.</p>	<p>2. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills. 4. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context</p>	<p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement. 8.2.1) The ability to Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.</p>	<p>CITY2075 Electrical Power CITY2076 Further Analogue Electronics CITY2077 Further Digital Electronics CITY2078 Applications of Power Electronics CITY2079 Work-based Learning Project CITY2081 Industrial Control Systems and Mechatronics</p>

<p>Knowledge of the main methods of enquiry in the subject relevant to the named award, and ability to evaluate critically the appropriateness of different approaches to solving problems in the field of study;</p>	<p>B2, Contribute to the design and development of engineering solutions. B3, Implement design solutions and contribute to their evaluation.</p> <hr/> <p>A1, Maintain and extend a sound theoretical approach to the application of technology in engineering practice. A2, Use a sound evidence-based approach to problem-solving and contribute to continuous improvement. B2, Contribute to the design and development of engineering solutions. B3, Implement design solutions and contribute to their evaluation.</p>	<p>of electrical and electronic design.</p> <hr/> <p>1. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills. 2. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems. 4. Provide the opportunity to 'learn through</p>	<p>8.2.2) The ability to use results of analysis to solve electrical and electronic engineering problems, apply technology and implement solutions. 8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data. 8.5.1) The ability to select and use appropriate equipment to perform engineering tests. 8.5.2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits. 8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to achieve flexibility within the work environment.</p> <hr/> <p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement. 8.5.1) The ability to select and use appropriate equipment to perform engineering tests. 8.5.2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits. 8.2.1) The ability to Identify, review and select techniques, procedures and methods to</p>	<hr/> <p>CITY2075 Electrical Power CITY2078 Applications of Power Electronics CITY2079 Workbased Learning Project CITY2081 Industrial Control Systems and Mechatronics</p>
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<p>An understanding of the limits of the knowledge, and how this influences analyses and interpretations based on that knowledge</p>	<p>C1, Plan for effective project implementation.</p> <hr/> <p>A2, Use a sound evidence-based approach to problem-solving and contribute to continuous improvement.</p>	<p>design' via practical and project based work, particularly within the context of electrical and electronic design.</p> <hr/> <p>1. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p> <p>4. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.</p>	<p>undertake electrical / electronic engineering tasks.</p> <p>8.2.2) The ability to use results of analysis to solve electrical and electronic engineering problems, apply technology and implement solutions.</p> <p>8.2.3) The ability to Implement design solutions and <u>contribute to their evaluation.</u></p> <p>8.1.1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice.</p> <p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.</p> <p>8.2.2) The ability to use results of analysis to solve electrical and electronic engineering problems, apply technology and implement solutions.</p> <p>8.2.3) The ability to Implement design solutions and contribute to their evaluation.</p> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.</p> <p>8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to</p>	<hr/> <p>CITY2079 Work-based Learning Project</p>
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			achieve flexibility within the work environment.	
<p>Students will be able to:</p> <p>Use a range of established techniques to initiate and undertake critical analysis of information, and to propose solutions to problems arising from that analysis;</p>	<p>A2, Use a sound evidence-based approach to problem-solving and contribute to continuous improvement.</p> <p>B1, Identify, review and select techniques, procedures and methods to undertake engineering tasks.</p> <p>B3, Implement design solutions and contribute to their evaluation.</p>	<p>1. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<p>8.1.1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice.</p> <p>8.1.2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain, decommission and re-cycle electrical / electronic engineering processes, systems, services and products.</p> <p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.</p> <p>8.2.1) The ability to Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.</p> <p>8.2.2) The ability to use results of analysis to solve electrical and electronic engineering problems, apply technology and implement solutions.</p> <p>8.2.3) The ability to Implement design solutions and contribute to their evaluation.</p>	<p>CITY2078 Applications of Power Electronics</p> <p>CITY2079 Work-based Learning Project</p> <p>CITY2081 Industrial Control Systems and Mechatronics</p>

<p>Effectively communicate information, arguments and analysis in a variety of forms to specialist and non-specialist audiences, and deploy key techniques of the discipline effectively;</p> <p>Undertake further training, develop existing skills and acquire new competences that will enable them to assume significant responsibility within organisations.</p>	<p>D1, Communicate in English² with others at all levels. D2, Present and discuss proposals. D3, Demonstrate personal and social skills.</p> <p>E4, Carry out and record CPD necessary to maintain and enhance competence in own area of practice.</p>	<p>4. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.</p> <p>2. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems.</p>	<p>8.5.1) The ability to select and use appropriate equipment to perform engineering tests. 8.5.2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits.</p> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data. 8.5.2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits.</p> <p>8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to achieve flexibility within the work environment. 8.4.3) Appropriate Work based learning through Projects and practical assessments.</p>	<p>CITY2079 Work-based Learning Project</p> <p>CITY2075 Electrical Power CITY2076 Further Analogue Electronics CITY2077 Further Digital Electronics CITY2078 Applications of Power Electronics CITY2079 Work-based Learning Project CITY2081 Industrial Control Systems and Mechatronics</p>
<p>Students will also have:</p> <p>The qualities and transferable skills necessary for employment requiring the exercise of personal responsibility and decision-making</p>	<p>E4, Carry out and record CPD necessary to maintain and enhance competence in own area of practice. E5, Exercise responsibilities in an ethical manner.</p>	<p>1. Establish broad foundation knowledge on which to develop further skills as technology advances and to enable students to apply engineering principles to the analysis and design of electronic, electric power and renewable energy systems.</p>	<p>8.1.1) A sound theoretical approach to the application of technology in electrical / electronic engineering practice. 8.1.2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain,</p>	<p>CITY2075 Electrical Power CITY2076 Further Analogue Electronics CITY2077 Further Digital Electronics CITY2078 Applications of Power Electronics CITY2079 Work-based Learning Project CITY2081 Industrial Control Systems and Mechatronics</p>

		<p>2. Provide the opportunity to 'learn through design' via practical and project based work, particularly within the context of electrical and electronic design.</p> <p>3. Provide an awareness of the business implications of engineering decisions and a knowledge of the inter-relationship between the market, engineering activities and the management structures</p> <p>4. Provide the opportunity to develop communication, data collection and analysis, ingenuity, problem solving, application and diagnostic skills.</p>	<p>decommission and re-cycle electrical / electronic engineering processes, systems, services and products.</p> <p>8.1.3) A sound evidence-based approach to problem-solving and contribute to continuous improvement.</p> <p>8.2.1) The ability to Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.</p> <p>8.2.2) The ability to use results of analysis to solve electrical and electronic engineering problems, apply technology and implement solutions.</p> <p>8.2.3) The ability to Implement design solutions and contribute to their evaluation</p> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.</p> <p>8.3.2) Work independently or as a member of a team.</p> <p>8.4.1) Good student centred learning skills which will promote lifelong learning and a commitment to continuing professional development to achieve flexibility within the work environment.</p> <p>8.4.2) Knowledge of relevant codes of conduct.</p>	
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			<p>8.4.3) Appropriate Work based learning through Projects and practical assessments.</p> <p>8.5.1) The ability to select and use appropriate equipment to perform engineering tests.</p> <p>8.5.2) The ability to monitor, analyse and evaluate the performance of electrical and electronic circuits.</p>	
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Appendix 13.2 Assessment against modules Map

	CITY1077 Engineering Mathematics (Core)	CITY1078 Engineering Science (Core)	CITY1079 Digital and Analogue Devices (Core)	CITY1081 Electrical and Electronic Principles (Core)	CITY1080 Project Design and Business Management (Core)	CITY1082 Microprocessor Systems and High Level Programming (Core)	CITY2075 Electrical Power (Core)	City2076 Further Analogue Electronics (Core)	CITY2077 Further Digital Electronics (Core)	CITY2078 Application of Power Electronics (Core)	CITY2079 Work-based Learning Project (Core)	CITY2081 Industrial Control systems and Mechatronics (Core)
Essay												
Report		✓			✓	✓			✓			✓
Engineering Problem Assignment	✓			✓			✓			✓		
Portfolio											✓	
Exam	✓	✓	✓	✓			✓			✓		
In Class Test								✓				
Practical								✓				✓
Presentation			✓		✓	✓			✓		✓	

Appendix 13.3 Skills against modules Map

	Engineering Mathematics (Core)	Engineering Science (Core)	Digital and Analogue Devices (Core)	Electrical and Electronic Principles (Core)	Project Design and Business Management (Core)	Microprocessor Systems and High Level Programming (Core)	Electrical Power (Core)	Further Analogue Electronics (Core)	Further Digital Electronics (Core)	Application of Power Electronics (Core)	Work-based Learning Project (Core)	Industrial Control systems and Mechatronics (Core)
Essay Writing												
Report Writing		✓			✓	✓			✓			✓
Presentation skills			✓		✓	✓			✓		✓	
Project Planning / Management					✓						✓	✓
Research	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
IT Skills			✓	✓	✓	✓		✓	✓		✓	✓
Team Work					✓						✓	✓
Evaluation	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Data Analysis	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓

Appendix 13.4 Work based Learning Map

FHEQ level: 5				
WBL Activity	Prog Intended LO	Related Modules	Assessed LO	Range of Assessments
Presentation of work based scenario	<p>8.1.2) Appropriate theory and practical skills to design, develop, manufacture, construct, commission, operate, maintain, decommission and re-cycle electrical / electronic engineering processes, systems, services and products.</p> <p>8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.</p>	CITY1080 Project Design and Business Management	<p>LO3. Demonstrate an understanding of ethical sustainability for waste management, carbon allowance and carbon footprint reduction methodologies.</p> <p>LO4. Demonstrate an understanding of the practical and commercial constraints affecting the design and management of renewable and distributed energy generation.</p>	P1 – Presentation of evidence
Industrial visit/Guest lecturer	8.4.1) Good student centred learning skills which will promote lifelong learning and a	CITY2075 Electrical Power	LO1: Describe and compare methods of conventional and alternative power generation.	C1 – Coursework guided by Industrial liaison

	<p>commitment to continuing professional development to achieve flexibility within the work environment.</p> <p>8.4.2) Knowledge of relevant codes of conduct.</p> <p>8.4.3) Appropriate Work based learning through Projects and practical assessments.</p>		<p>LO2: Appraise and analyse power transformers and their application.</p> <p>LO3: Evaluate and analyse power distribution circuits, configuration, testing and utilisation</p> <p>LO4: Investigate and analyse AC electrical machines , applications and operating characteristics.</p> <p>LO5: Investigate and analyse DC electrical machines and associated control systems.</p>	<p>P1 – Practical, work-based activities</p>
		<p>CITY2078 Applications of Power Electronics</p>	<p>LO1. Describe the construction and evaluate the operation of power electronic devices.</p> <p>LO2. Analyse and evaluate the circuit configurations used in single and polyphase rectifiers and converters.</p> <p>LO3. Describe various circuit configurations of Linear and Switched Mode power supplies.</p> <p>LO4. Describe and compare various circuit configurations to achieve speed control of AC and DC motors.</p> <p>LO5. Describe harmonic generation as well as</p>	

			harmonic reduction in industrial plant and the principles governing EMC.	
		CITY2079 Work-based Learning Project	LO1: Research, specify, design, build and test an engineering project in a discipline specific area. LO2: Construct a hardware prototype and test to specification. LO3: Design and implement software to interface with hardware designed. LO4: Write a technical report and give a formal presentation. LO5: Develop the project further to include enhanced technical elements.	
		CITY2081 Industrial Control Systems and Mechatronics	LO2 To develop an understanding of the components in a typical mechatronic system LO3 To develop an understanding of programming techniques used in mechatronic systems LO4 To design and evaluate a simple mechatronic system	

Trade fair style presentation (Employers to be invited)	8.2.1) The ability to Identify, review and select techniques, procedures and methods to undertake electrical / electronic engineering tasks.	CITY2079 Work-based Learning Project	LO3: Design and implement software to interface with hardware designed. LO4: Write a technical report and give a formal presentation.	P1 – Presentation of work-based designs
	8.2.3) The ability to Implement design solutions and contribute to their evaluation. 8.3.1) Communicate ideas and information; through verbal and written forms using appropriate terminology and presentation of data.	CITY2081 Industrial Control Systems and Mechatronics	LO3 To develop an understanding of programming techniques used in mechatronic systems LO4 To design and evaluate a simple mechatronic system	
Where possible, assessment will be supported either by workplace visits or guest lecturers from within industry. Practical elements will be tailored to specific students to ensure that employed students will carry out activities related to their workplace; Full time students will be encouraged to liaise with local employers to guide their choice of project to ensure currency with local workplace requirements.				

3. Module Records

SECTION A: DEFINITIVE MODULE RECORD. Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.

MODULE CODE: CITY 1077	MODULE TITLE: Engineering Mathematics
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: G160
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

To develop the student's mathematical ability and to apply principles to the solution of engineering problems and to make use of mathematical computer based packages.

This exam will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the exam, government guidelines on social distancing make this inappropriate the exam will take place remotely online. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT

WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	50%	C1	50%	P1	
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL Technology

Professional body minimum pass mark requirement: n/a

MODULE AIMS:

- To gain a solid foundation in algebra, trigonometry, functions and calculus in order to associate and recognise the importance of mathematics in the analysis of engineering problems
- To develop mathematical problem solving simultaneously with other science and engineering modules.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

- LO1. use basic mathematical techniques to solve engineering problems of an electrical, mechanical or civil engineering nature.
- LO2. recognise and solve 1st and 2nd order ordinary differential equations
- LO3. understand the use of complex number and matrix theory in practical engineering applications
- LO4. understand a variety of techniques of differential and integral calculus to calculate various functions in their associated applications in engineering

DATE OF APPROVAL: May 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sep 2017	SCHOOL/PARTNER: City College Plymouth
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications

<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>

- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Owais Raja	OTHER MODULE STAFF: N/A
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<p>Summary of Module Content</p> <p>Revision of Algebra and Arithmetic Basic number and arithmetic operations, algebraic techniques including evaluation of formula, rearranging formula, solving simple equations, laws of logarithms, laws of indices, etc. These skills will be built upon throughout the delivery of each individual topic in this module.</p> <p>Trigonometric functions and graphs Simple trigonometric functions of sine, cosine, tangent and hyperbolic functions of \sinh^{-1}, \cosh^{-1} and \tanh^{-1}. The applications of these functions in engineering including vectors and waveform combination.</p> <p>Complex numbers Addition, subtraction, multiplication and division of complex numbers in Polar and Cartesian form. The Argand diagram. The modulus and argument. Applications in engineering.</p> <p>Differential Calculus Basic differentiation techniques of polynomial, trigonometric, exponential and logarithmic functions. Further techniques including the product, quotient and chain rules. Engineering applications to optimisation and higher order differentials.</p> <p>Integral calculus Basic integration techniques of polynomial, trigonometric and exponential functions. Further techniques including integration by parts and substitution. The methodical applications of definite and indefinite integration with and without engineering scenarios including the interpretation of areas under a curve.</p> <p>Matrices General arithmetic operations on matrices. Solve equations by using the inverse matrix method and apply to engineering problems. Understand the different types of solutions: no, unique and infinite solutions. Diagonalisation to find eigenvalues and corresponding eigenvectors.</p>

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hour lectures
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	Module Examination	100%	LO1,3

Coursework	C1	Assignment	100%	LO2,4
			N/A	

Updated by: Owais Raja Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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Recommended Texts and Sources

The recommended texts for the course are:

Kuldeep Singh (2011) *Engineering Mathematics Through Applications* [Paperback] Palgrave Macmillan; 2nd edition

Stroud, K.A. and Booth, D.J. (2013) *Engineering mathematics*. 7th edn. Basingstoke: Palgrave Macmillan.

Stroud, K.A. and Booth, D.J. (2011) *Advanced engineering mathematics*. 5th edn. Basingstoke: Palgrave Macmillan.

Bird, J. (2014) *Basic engineering mathematics*. 6th edn. London: Routledge.

Bird, J. (2017) *Higher engineering mathematics*. 7th edn. United Kingdom: Routledge.

Greater Manchester university (no date) Available at:
<http://www.cse.salford.ac.uk/physics/gsmcdonald/PPLATO.php>

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY1078	MODULE TITLE: Engineering Science
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: H100
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

An introduction to mechanical principles, energy transfer and AC electrical theory. Mechanical principles including solid mechanics, statics, dynamics and mechanical vibrations. Modes of heat transfer and energy losses. Electrical principles and single phase AC theory.

This exam will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the exam, government guidelines on social distancing make this inappropriate the exam will take place remotely online. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	50%	C1	50%	P1	
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement: n/a

- MODULE AIMS:**
- To investigate the fundamental scientific principles which underpin the design and operation of engineering systems.
 - To give a mechanical and electrical overview which will provide the basis for further study in specialist areas of engineering.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

LO1. Demonstrate an understanding of basic static and dynamic mechanical systems
LO2. Investigate energy transfer in thermal and fluid systems
LO3. Recognise and recall how DC theory relates to simple electrical machines
LO4. Show knowledge and awareness of the fundamental principles of single phase AC theory

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
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DATE OF IMPLEMENTATION: Sept 2017	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 115
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MODULE LEADER: George Audu	OTHER MODULE STAFF: Andrew Reed
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<p>Summary of Module Content Statics and Dynamics: SF and BM, bending stresses. Torsion . Uniform acceleration linear and angular. Newton’s laws of motion, mass moment of inertia, kinetic energy, effects of friction. Vibrations, SHM, forcing and damping. Energy Transfer: Heat transfer: conduction, convection, radiation, thermal conductivity, forced convection, black and grey body radiation. insulated surfaces. Viscosity: boundary layer formation, laminar and turbulent flow, pressure loss in pipes. Energy losses: dynamic viscosity, power loss in bearings. pipe friction losses. Electrical Principles: Conductors, insulators, voltage and current. Ohm’s law, Kirchhoff’s law. Power: Electro-magnetic induction, transformers, Lenz’s and Faraday’s laws. Generator and motor principles. Single Phase AC theory: Non-resonant circuits: R-C-L circuits; Argand diagrams. Resonant circuits, L-C series and parallel, resonant frequency, Power factor correction, Complex waveforms: graphical analysis, odd and even-harmonics, phase shift, non-linear characteristics.</p>
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SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	50	25 x 2 hour lectures
Lab time	10	5 x 2 hour lab time (Contact)
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	End of Module Examination	100%	LO1, LO2
Coursework	C1	Assignment (Report on in class experiments)	100%	LO3, LO4

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Bolton, W. (2004) Higher engineering science. 2nd edn. Amsterdam, [Pays-Bas]: Newnes (an imprint of Butterworth-Heinemann Ltd).

Tooley, M.H., Dingle, L., BA, M.T. and Technol., A. (2012) Engineering science: For foundation degree and higher national. New York: Elsevier Science.

Bacon, D H and Stephens, R C (2000) Mechanical technology, Industrial Press, New York

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY1079	MODULE TITLE: Digital and Analogue Devices and Circuits
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: H651
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

This module provides learners with a practical understanding of a range of digital and analogue devices and circuits in common use within Electrical/Electronic Engineering Systems. Students will analyse the operational principles associated with a number of fundamental electronic building blocks and will consolidate their learning through the practical build, testing and presentation of real circuits. This exam and practical will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the exam, government guidelines on social distancing make this inappropriate the exam/ practical will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	50%	C1		P1	50%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL : Technology

Professional body minimum pass mark requirement: n/a

- MODULE AIMS:**
- To give the learner a sound knowledge of the operational principles of a range of digital and analogue devices and circuits
 - To develop the skills necessary to design construct and test common analogue and digital circuits.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

LO1. Describe the operation and characteristics of arrange of analogue devices and circuits

LO2. Describe the operation and use of a range of logic devices

LO3. Design and test, using computer simulation and/or practical build an analogue circuit to a given specification

LO4. Design and test, using computer simulation and/or practical build a digital circuit to a given specification

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
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DATE OF IMPLEMENTATION: Sept 2017	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF: George Audu
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<p>Summary of Module Content</p> <p>Devices – dc and small signal operation of diodes and transistors, DC power supplies – operation, design and test of linear and switched mode power supplies.</p> <p>Operational amplifiers – ideal and practical op-amps, operation, design and test of common operational amplifier circuits, use of simulation software.</p> <p>Digital electronic circuits – logic devices and elements, combinational logic design, sequential logic circuit design, use of simulation software.</p>
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SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	44	22 x 2hr sessions
Lab Work	16	8 x 2hr lab sessions
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	End of Module Examination	100%	LO1, LO2
Practice	P1	Presentation of digital circuit design and operation	100% 50% - presentation 50% - Supporting documentation (e.g. poster/handout)	LO3, LO4

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Floyd, T.L. (2014) *Digital fundamentals*. 11th edn. Boston, MA, United States: Prentice Hall.

Learn about electronics - home page (2016) Available at: <http://www.learnabout-electronics.org/> (Accessed: 21 November 2016).

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY1080	MODULE TITLE: Project Design and Business Management.
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: H221
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PRE-REQUISITES: None	CO-REQUISITES: None	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This module introduces concepts of current energy / business / project management techniques in accordance with current professional practice within the engineering sector. The module is project and case study based, investigating different industrial scenarios.
 This practical assessment will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1	60%	P1	40%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement:

MODULE AIMS:
 To develop awareness of current business / project management techniques.
 Investigate techniques to enhance the efficient use of energy and carbon footprint reduction methodologies. Investigate the management of distributed energy generation, energy conservation and business practice involving developing renewable energy technologies / protocols.

ASSESSED LEARNING OUTCOMES: (additional guidance below)
 At the end of a module the learner will be expected to be able to:

- LO1.** Identify and describe energy costing methodologies and the implications for the provision of engineering services.
- LO2.** Apply current business / marketing / project management techniques in an ethical and effective manner.
- LO3.** Demonstrate an understanding of ethical sustainability for waste management , carbon allowance and carbon footprint reduction methodologies.
- LO4.** Demonstrate an understanding of the practical and commercial constraints affecting the design and management of renewable and distributed energy generation.

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2017	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: George Audu	OTHER MODULE STAFF:
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Summary of Module Content
 Energy measurement, loading and costing techniques, energy and business management processes e.g. reduction, recovery and recycling in practice. Project leadership, financial and resource management techniques. Current and developing procedures in ethical sustainability for waste management and carbon allowance. Business management and costing techniques, carbon foot-printing, energy trading, emission / pollutant management. The basic concepts of distributed energy management, sustainable energy resources, and energy collection techniques.

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hours sessions
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Directed self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Coursework	C1	Case Study	100%	LO1, LO2
Practice	P1	Presentation	100%	LO3, LO4

Updated by: George Audu Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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Capehart, B.L., Turner, W.C. and Kennedy, W.J. (2016) *Guide to energy management*. United States: Productivity Press.
 Harris, D.J. (2011) *A guide to energy management in buildings*. London, United Kingdom: Taylor & Francis.
 Nicholas, J.M. and Steyn, H. (2011) *Project management for engineering, business and technology*. 4th edn. New York, NY: Butterworth-Heinemann.

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY1081	MODULE TITLE: Electrical & Electronic Principles
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: H600
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PRE-REQUISITES: None	CO-REQUISITES: None	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This module covers the Electrical Principles which learners in many branches of Electrical and Electronic Engineering need to understand. It builds on the elements of basic circuit theory and provides the basis for further study in the more specialist areas of Electrical and Electronic Engineering.
 This exam will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	60%	C1	40%	P1	
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL : Technology

Professional body minimum pass mark requirement: n/a

MODULE AIMS:
 The aim of this module is to develop the skills necessary to analyse circuits and waveforms, by gaining an understanding of the principles of circuit theory, the behaviour of passive and reactive components, two-port networks, complex waves and circuit transients.

ASSESSED LEARNING OUTCOMES: (additional guidance below)
 At the end of the module the learner will be expected to be able to:
LO1. Demonstrate an understanding of dc circuit theorems and be able to apply them to solve practical circuit problems. Understand the application of vectors and complex numbers to the solution of ac circuits.
LO2. Investigate and develop analytical models of transformers and two-port networks.
LO3. Demonstrate an understanding of the analysis and synthesis of complex waveforms.
LO4. Develop an understanding of the analysis of circuit transients.

DATE OF APPROVAL: June 2017	FACULTY: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2017	PARTNER: City College Plymouth
DATE(S) OF APPROVED CHANGE:	TERM: All Year
Additional notes (for office use only):	



Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

• **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: George Audu	OTHER MODULE STAFF: Andrew Reed
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<p>Summary of Module Content Circuit Theory-transformation theorems and equivalent circuit parameters, circuit theorems, magnetically coupled circuits and series and parallel tuned circuits. Two-port networks-network models applied to practical circuits, transformers, modelling of common two-port networks. Complex waves-properties, analysis and synthesis of complex waves. Laplace transforms-definition, use of transform tables, solution of first order systems for step and sinusoidal inputs, solution of second order systems to step inputs.</p>
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SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hour lectures
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	End of Module Examination	100%	LO1,LO2, LO4
Coursework	C1	Assignment-Analysis of complex AC wave form	100%	LO3

Updated by: George Audu Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for this course are:

Hughes, E., Hiley, J. and McKenzie-Smith, I. (2016) *Hughes electrical and electronic technology*. Harlow, United Kingdom: Pearson Education.
 Bird, J. (2013) *Electrical and electronic principles and technology*. 5th edn. London, United Kingdom: Routledge.
 Bird, J. (2013) *Electrical circuit theory and technology*. 5th edn. London: Routledge.

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY1082	MODULE TITLE: Microprocessor Systems & High Level Programming
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CREDITS: 20	FHEQ LEVEL: 4	JACS CODE: H221
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This unit is intended to give learners an understanding of the general principles and concepts of programming in high level language, to create and test simple programs capable of interfacing with external hardware.
 This practical assessment will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1	50%	P1	50%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL :Technology

Professional body minimum pass mark requirement: n/a

- MODULE AIMS:**
- To investigate the characteristics and use of microcontroller systems.
 - To investigate microprocessor interfacing and communication methods.
 - To design and develop high level code using structured programming methods.
 - To create and apply test schedules for a given application.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

LO1: Describe the internal architecture of a typical microprocessor/microcontroller system
 LO2: Describe the interfacing and communication methods used to interact with a range of external hardware devices
 LO3: Produce software to allow a microprocessor system to interact with external hardware using a structured design technique.
 LO4: Use an appropriate development environment to implement, error check and test software compliance against a specification

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2017	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE: May 2017	TERM: All year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF:
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<p>Summary of Module Content</p> <p>Microcontroller architecture and instruction set – ALU, RAM, ROM, stack, etc., Bus architecture, use of registers and embedded features, e.g. timers, ADC, comparators. RISC architecture. Program design – use of an algorithmic approach, e.g. structure charts, pseudo code Write program – use of a high level language and software debugging tools e.g. Integrated Development Environment (IDE) and simulation. Data storage - Integers, floating-point, characters, Boolean, strings, arrays and files. Program structures – Iterative and selection structures, functions / procedures. Programming standards – appropriate syntax, use of comments, layout e.g. indentation and descriptive identifiers. Test schedules – error types; test data, plan and record of testing</p>

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	40	20 x 2 hour lectures
Practical	20	10 x 2 hour practical labs
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Directed self-study
Tota	200	

Category	E l e m e n t	Compone nt Name	Component weighting	Comments Include links to learning objectives
Coursework	C1	Report 1	100%	LO1, LO2 Structured report
Practice	P1	Presentation of	50% - Supporting documentation (e.g. poster/handout)	LO3, LO4 Report on design methodology and outcomes of testing

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Crisp, J. (2003), Introduction to microprocessors and microcontrollers, 2nd edition. Amsterdam: Newnes (an imprint of Butterworth-Heinemann Ltd).
Dumas II, J. (2017). Computer Architecture. Boca Raton: CRC Press. ISBN: 9781498772723
Gil de Lamadrid, J. (2018). Computer Organization, New York: Chapman and Hall/CRC.
ISBN:9781351999755
Rafiquzzaman, M. Microcontroller theory and applications with the PIC18F. ISBN: 9780470947692-
Publication Date: 2011
Tahir, M., Javed, K. (2017). ARM Microprocessor Systems. Boca Raton: CRC Press. ISBN:9781482259391

Websites:

Pic Programming in C, Available at:

<https://www.mikroe.com/ebooks/pic-microcontrollers-programming-in-c/introduction> (Accessed: July 2021)

Arduino (2020) Home. Available at: <http://www.arduino.cc/>(Accessed: July 2021).

Cypress PSoC 6 Website

https://www.cypress.com/documentation/development-kitsboards/psoc-6-wifi-bt-pioneer-kit-cy8ckit-062-wifi-bt?source=search&cat=software_tools (Accessed: July 2021).

Nucleo F767ZI home. Available at: <https://www.st.com/en/evaluation-tools/nucleo-f767zi.html#resource> (Accessed: July 2021).

Sololearn C++ Tutorial site: <HTTps://www.sololearn.com/CPlusPlus/> (Accessed: July 2021)

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY2075	MODULE TITLE: Electrical Power
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H630
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

This module provides introduction to the principles, techniques and applications of electrical power engineering. The student will evaluate and analyse conventional and alternative electricity generation and utilisation, distribution, energy management and control systems. The student will also analyse AC and DC motors and generators, single and poly-phase transformers and associated device protection. This exam will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT

WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	60%	C1	40%	P1	
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement: n/a

MODULE AIMS:

- Provide an overview of conventional and alternative electricity generation
- Provide an ability to analyse and describe methods of power utilisation, distribution, energy management and control methodology.
- Provide an ability to analyse single and poly-phase transformers within power systems.
- Provide an understanding of AC and DC electrical machines, their characteristics and applications

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner will be expected to be able to:

- LO1: Describe and compare methods of conventional and alternative power generation.
 LO2: Appraise and analyse power transformers and their application.
 LO3: Evaluate and analyse power distribution circuits, configuration, testing and utilisation
 LO4: Investigate and analyse AC electrical machines , applications and operating characteristics.
 LO5: Investigate and analyse DC electrical machines and associated control systems.

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2018	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: George Audu	OTHER MODULE STAFF:
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<p>Summary of Module Content Conventional and alternative power generation and transmission. Methods of power distribution (radial, ring, parallel) and protection, power factor correction. Three phase systems – analysis of symmetrical and non-symmetrical current flow. Single and poly-phase power transformers, operation, construction, operating principles, load / no load conditions, connection configuration and utilisation. Operation, construction and control of Shunt, Series and Compound wound DC motors and generators. Construction operation and analysis of Induction and Synchronous AC motors and generators. Construction, application and control of AC “universal” motors.</p>

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hours delivered sessions
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	End of Module Examination	100%	LO2, LO4, LO5
	C1	Case Study	100%	LO1, LO3

Updated by: George Audu Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Hughes, A., Drury, B. and Drury, W. (2013) *Electric motors and drives: Fundamentals, types and applications*. 4th edn. Oxford: Newnes (an imprint of Butterworth-Heinemann Ltd).

Hughes, E., Hiley, J. and McKenzie-Smith, I. (2016) *Hughes electrical and electronic technology*. Harlow, United Kingdom: Pearson Education.

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY2076	MODULE TITLE: Further Analogue Electronics
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H652
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

This module extends the theory and practice of analogue circuits covered in previous studies. It will aim to link previously learnt theory into the practice of designing, building and testing real analogue circuits to given specifications.

This practical and In Class Test will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then they will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT

WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1		P1	75%
E2 (OSCE)		C2		P3	
T1 (in-class test)	25%	A1			

SUBJECT ASSESSMENT PANEL : Technology

Professional body minimum pass mark requirement: n/a

MODULE AIMS:

- To impart a sound knowledge of the operational principles of common analogue circuits.
- To develop the skills necessary to analyse, simulate and construct analogue circuits to given specifications

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

1. Describe and analyse the effect of various types of feedback on analogue circuit operation and performance.
2. Design, simulate and construct common amplifier configurations.
3. Design, simulate and construct common oscillator circuits.
4. Design, simulate, and construct basic active filter configurations.

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2018	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

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ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF: George Audu
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<p>Summary of Module Content</p> <p>Types of feedback -effect on circuit performance (gain, stability, bandwidth, distortion, noise and input and output impedance).</p> <p>Types of amplifier- operational, power, tuned, analysis using equivalent circuits and software modelling. Amplifier implementation to given specifications.</p> <p>Oscillators- conditions for oscillation, common oscillator types, analysis and design of oscillators to meet given specifications.</p> <p>Active filters-filter types and responses, analysis and design of common filters to meet given specifications</p>

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	50	25 x 2 hour delivered sessions
Practical Sessions	10	5 x 2 hour practical circuit building
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments <i>Include links to learning objectives</i>
Written exam				
	T1	In Class Test	100%	LO1
Practice				
	P1	Presentation	100%	LO2, LO3, LO4 Presentation of final design solutions with appropriate supporting documentation

<p>Updated by: Andrew Reed Date: August 2025</p>	<p>Approved by: Hollie Galpin-Mitchell Date: August 2025</p>
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The recommended texts for the course are:

Clayton, G and Winder, S (2003) Operational amplifiers, Butterworth-Heinemann, London

Floyd, T. (2018) Electronic Devices (Conventional Current Version), 10th Edition, Pearson.

Tooley, M (2019) Electronic circuits: fundamentals and applications, Routledge

Bishop, O (2011) Electronics: circuits and systems, Newnes, Oxford

E-books and additional reading

Clayton, G and Winder, S, (2003) Operational amplifiers, (electronic book) Elsevier Science Newnes, Oxford. Available at:

https://www.academia.edu/40454776/Operational_Amplifiers_5th_Edition_George_Clayton_Steve_Winder?auto=download (accessed date 07/07/2021)

Tooley, M (2019) Electronic circuits: fundamentals and applications, (electronic book) Elsevier Science Newnes, Oxford. Available at:

<https://www.routledge.com/Electronic-Circuits-Fundamentals-and-Applications/Tooley/p/book/9780367421984> (Accessed date 07/07/2021)

Bishop, O (2011) Electronics circuits and systems, (electronic book) Newnes, Oxford. Available at:

http://bookshop.blackwell.co.uk/jsp/id/Electronics_Circuits_and_Systems/9780080966342 (Accessed date 07/07/2021)

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY2077	MODULE TITLE: Further Digital Electronics
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H221
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This module is intended to develop combinational and sequential logic design techniques, making use of industry standard Electronic CAD tools, and to introduce software for Programmable Logic Devices. This practical assessment will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1	50%	P1	50%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL : Technology

Professional body minimum pass mark requirement: n/a

- MODULE AIMS:**
- To use formal design techniques to design advanced digital circuits.
 - To introduce professional standard CAD design and simulation tools.
 - To use computer simulation to verify logic designs.
 - To use programmable logic devices to minimise component count
 - To design and evaluate a digital system
 - To introduce VHDL for the design and simulation of a hardware system.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

LO1: Use formal design techniques to design advanced digital circuits to a given specification.
 LO2: Use computer based tools to design, simulate and verify digital logic designs
 LO3: Demonstrate an understanding of the use and design of PLD systems.
 LO4: Demonstrate an understanding of digital logic design using VHDL.

DATE OF APPROVAL: Feb 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2017	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: ALL YEAR

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF: George Audu
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<p>Summary of Module Content</p> <p>Minimisation techniques – e.g. Boolean algebra, Karnaugh maps, Quine-McCluskey technique. Sequential logic devices – asynchronous and synchronous counters, minimization and elimination of hazards. Design techniques – Finite State machines, Moore Mealy models. Electronic CAD – use of software for simulation and verification of designs. Program Logic Devices – use of PLDs to reduce component count. VHDL for programmable logic, use of the development environment.</p>
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SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hour delivered sessions
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Coursework	C1	Report 1	100%	LO1, LO2
Practice	P1	Presentation of VHDL design and methodology	50% - presentation 50% - Supporting documentation (e.g. poster/handout)	LO3, LO4

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Arockia Bazil Raj, A. (2018), FPGA-Based Embedded System Developer’s Guide, CRC Press, ISBN: 9781498796750

Ashenden, P J (2006), The designer's guide to VHDL (Third Edition), Elsevier Science, San Francisco. ISBN: 9780120887859

Floyd, T.L. (2015), Digital fundamentals. Harlow, United Kingdom: Pearson Education.

Green, D C (1999), Applied digital electronics, Pearson Education, Harlow.

Kinney, L and Roth, C H (2014) Fundamentals of logic design International Edition, Cengage Learning, Florence. ISBN: 9781133628484

Pedroni, V.A. (2010), Circuit design and simulation with VHDL. Cambridge, MA: MIT Press.

Roth, C.H. and Kinney, L. (2013), Fundamentals of logic design. 7th edition. Independence, KY: Nelson Engineering.

Sarkar, S (2014), Foundation of Digital Electronics and Logic Design, Pan Stanford, ISBN:9789814364584

Online resources:

Free Range VHDL: Factory, F.R. (2016) Free range factory. Available at: <http://freerangefactory.org/> (Accessed: July 2021).

Altera Quartus University Program (For Quartus 19.0):
(May 2021) Available at: <https://www.altera.com/support/training/university/materials-tutorials.html>
(Accessed: July 2021).

DEEDS Digital Electronics Deeds, Tutorials, VHDL and Simulation on FPGAs,
<https://www.digitalelectronicsdeeds.com/index.html> (Accessed: July 2021).

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY2078	MODULE TITLE: Applications of Power Electronics
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H610
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This module provides introduction to the principles, techniques and applications of power electronics. After an initial exploration of power device characteristics and application the student will be evaluate and analyse electronic power circuits for the control of AC/DC motors, Inverters, SMPU, industrial control and protection.
 This exam will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)	60%	C1	40%	P1	
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement:

- MODULE AIMS:**
- Provide a sound understanding of the role and application of power electronic devices.
 - To analyse and evaluate power devices and circuits taking into account harmonic generation and circuit protection.
 - To describe, compare and evaluate power control circuits for industrial applications.
 - To describe and evaluate power circuits for the control of motors and plant.
 - Provide a basic understanding of the role and application of EMC.

ASSESSED LEARNING OUTCOMES: (additional guidance below)
 At the end of the module the learner will be expected to be able to:

LO1. Describe the construction and evaluate the operation of power electronic devices.
 LO2. Analyse and evaluate the circuit configurations used in single and polyphase rectifiers and converters.
 LO3. Describe various circuit configurations of Linear and Switched Mode power supplies.
 LO4. Describe and compare various circuit configurations to achieve speed control of AC and DC motors.
 LO5. Describe harmonic generation as well as harmonic reduction in industrial plant and the principles governing EMC.

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
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DATE OF IMPLEMENTATION: Sept 2018	SCHOOL/PARTNER: CCP
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DATE(S) OF APPROVED CHANGE:	TERM: All Year
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Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

● **SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT**

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 20
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MODULE LEADER: George Audu	OTHER MODULE STAFF: Andrew Reed
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<p>Summary of Module Content Fabrication and operation of power electronic devices. Circuit configurations for single and multiphase rectification, converters and inverters. Linear and Switched Mode power supplies. PWM, Regulation, and power factor control. Methods of AC and DC electronic motor control, investigating differing circuit configurations. Basic analysis of harmonic generation, methods for limitation - including passive and active filtering. Circuit protection techniques and power dissipation management. Principles and methods of EMC in industrial practice.</p>

SUMMARY OF TEACHING AND LEARNING		
Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hour delivered sessions
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Directed self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Written exam	E1	End of Module Examination	100%	LO1, LO2, LO3
Coursework	C1	Assignment	100%	LO4, LO5

Updated by: George Audu Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Barnes, M. (2003) Practical variable speed drives and power electronics. Oxford: Newnes (an imprint of Butterworth-Heinemann Ltd).

Lander, C.W. and L, C.W. (1993) Power electronics. 3rd edn. New York: McGraw Hill Higher Education.

Bradley, D.A. (1995) Power electronics: 1995. 2nd edn. London, Angleterre: Chapman and Hall.

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY 2079	MODULE TITLE: Work Based Learning Project
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H221
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:

This module enables students to gain specific skills in the design, build, test and evaluation of an engineering project. Thereafter the student will be given the opportunity to develop aspects of project design to include additional technical elements or functions. Generic skills include the use of industry standard CAD tools, project construction and software development. Emphasis will be placed upon preparation for 'real world' work related projects.

This practical assessment will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT [Use HESA KIS definitions]					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1	60%	P1	40%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement: n/a

MODULE AIMS:
The aim of this module is to enable students to research, specify, construct, design, evaluate and test a project in an engineering discipline.

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

LO1: Research, specify, design, build and test an engineering project in a discipline specific area.
 LO2: Construct a hardware prototype and test to specification.
 LO3: Design and implement software to interface with hardware designed.
 LO4: Write a technical report and give a formal presentation.
 LO5: Develop the project further to include enhanced technical elements.

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2018	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- *Subject benchmark statements*
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT

Items in this section must be considered annually and amended as appropriate, in conjunction with the Module Review Process. Some parts of this page may be used in the KIS return and published on the extranet as a guide for prospective students. Further details for current students should be provided in module guidance notes.

ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF: George Audu
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Summary of Module Content

- Research skills and project planning.
- Electronic design skills and analysis of circuit designs.
- Programming skills, development of software and use of IDE.
- Design, build and test a project prototype to a given specification.
- Write a program for the target system and ensure functionality to a given specification.
- Develop prototype using schematic capture, circuit simulation and layout of a basic electronic circuit prototype.
- Write a technical report and provide a formal presentation
- Develop the project further to include enhanced technical elements

SUMMARY OF TEACHING AND LEARNING

Scheduled Activities	Hours	Comments/Additional Information
Lecture	60	30 x 2 hour guided delivery and research
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Coursework	C1	Portfolio	100%	LO1, LO4 & LO5
Practice	P1	Presentation of prototype and software solutions	100% - Practical demonstration of hardware and software design solutions	LO2 & LO3

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Dym, C.L., Little, P., Orwin, E.J. and Mudd, H. (2014) *Engineering design: A project-based introduction*. 4th edn. New York: John Wiley & Sons.

Nicholas, J.M. and Steyn, H. (2011) *Project management for engineering, business and technology*. 4th edn. New York, NY: Butterworth-Heinemann.

SECTION A: DEFINITIVE MODULE RECORD. *Proposed changes must be submitted via Faculty Quality Procedures for approval and issue of new module code.*

MODULE CODE: CITY2081	MODULE TITLE: Industrial Control Systems and Mechatronics
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CREDITS: 20	FHEQ LEVEL: 5	JACS CODE: H730
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PRE-REQUISITES: N	CO-REQUISITES: N	COMPENSATABLE: Y
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SHORT MODULE DESCRIPTOR:
 This module will introduce a multidisciplinary view of typical mechatronic systems integrating a number of engineering disciplines.
 Learners will analyse individual elements before identifying the interface and control methods used to combine those elements into a complete system.
 Learners will then apply formal design methodologies to implement and test a simple mechatronic system.
 This practical assessment will take place in person and on-site at CCP as specified on the Assignment Brief. However, if at the time of the assessment, government guidelines on social distancing make this inappropriate then it will take place remotely online or other alternatives will be arranged. Any changes will be communicated via the DLE.

ELEMENTS OF ASSESSMENT					
WRITTEN EXAMINATION		COURSEWORK		PRACTICE	
E1 (Formally scheduled)		C1	50%	P1	50%
E2 (OSCE)		C2		P3	
T1 (in-class test)		A1			

SUBJECT ASSESSMENT PANEL: Technology

Professional body minimum pass mark requirement: n/a

- MODULE AIMS:**
- To introduce mathematical modelling of mechatronic systems
 - To develop an understanding of the components in a typical mechatronic system
 - To develop an understanding of programming techniques used in mechatronic systems
 - To design and evaluate a simple mechatronic system

ASSESSED LEARNING OUTCOMES: (additional guidance below)

At the end of a module the learner **will be expected to be able to:**

- LO1. Mathematically model a range of electro-mechanical components used in mechatronic systems
- LO2. Understand the use and operation of a range of mechatronic system components
- LO3. Apply programming techniques to mechatronic systems
- LO4. Design, implement and test an industrial control system to a given specification

DATE OF APPROVAL: June 2017	FACULTY/OFFICE: Academic Partnerships
DATE OF IMPLEMENTATION: Sept 2018	SCHOOL/PARTNER: CCP
DATE(S) OF APPROVED CHANGE:	TERM: All Year

Additional notes (for office use only):

Additional Guidance for Learning Outcomes:

To ensure that the module is pitched at the right level check your intended learning outcomes against the following nationally agreed standards

- Framework for Higher Education Qualifications
<http://www.qaa.ac.uk/Publications/InformationAndGuidance/Documents/FHEQ08.pdf>
- Subject benchmark statements
<http://www.qaa.ac.uk/ASSURINGSTANDARDSANDQUALITY/SUBJECT-GUIDANCE/Pages/Subject-benchmark-statements.aspx>
- SEEC level descriptors <http://www.seec.org.uk/academic-credit/seec-credit-level-descriptors-2010>
(scroll to pdf link at bottom of page)
- Professional, regulatory and statutory (PSRB) accreditation requirements (where necessary e.g. health and social care, medicine, engineering, psychology, architecture, teaching, law)
- QAA Quality Code
<http://www.qaa.ac.uk/AssuringStandardsAndQuality/quality-code/Pages/default.aspx>

SECTION B: DETAILS OF TEACHING, LEARNING AND ASSESSMENT

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ACADEMIC YEAR: 2025/26	NATIONAL COST CENTRE: 119
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MODULE LEADER: Andrew Reed	OTHER MODULE STAFF: George Audu
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Summary of Module Content

Derive mathematical models for electrical and mechanical systems: 1st and 2nd order systems; differential equations; characteristics; responses to inputs; comparisons with real systems
Selection of suitable components for mechatronic systems: Central processor selection, e.g. PIC, AVR, PLC, embedded system; sensor technologies, e.g. resistive, capacitive, inductive, optical, and ultrasonic; actuator technologies, e.g. motors, stepper motors and solenoid valves; Interfacing methods.
Programming techniques: Assembly language; High level languages, e.g. C, Flowcharts, Ladder logic
System design to a given specification: Formal design techniques to meet appropriate standards and regulations; selection of appropriate system components.

SUMMARY OF TEACHING AND LEARNING

Scheduled Activities	Hours	Comments/Additional Information
Lecture	40	20 x 2 hours delivery
Practical	20	10 x 2 hours design, build and test
Tutorial	15	Academic Support (Contact and VLE)
Independent Study	125	Guided self-study
Total	200	

Category	Element	Component Name	Component weighting	Comments Include links to learning objectives
Coursework	C1	Report on mechatronic theory	100%	LO1, LO2
Practice	P1	Presentation of practical development and testing with associated developmental documentation	100%	LO3, LO4

Updated by: Andrew Reed Date: August 2025	Approved by: Hollie Galpin-Mitchell Date: August 2025
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The recommended texts for the course are:

Bolton, W. (2015) *Mechatronics: Electronic control systems in mechanical and electrical engineering*. Harlow, United Kingdom: Pearson Education.

Bolton, W. (2015) *Programmable logic controllers*. Oxford, United Kingdom: Newnes (an imprint of Butterworth-Heinemann Ltd.)